

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Patent Application of:)
Zhang et al.) Examiner: Not Yet Assigned
Application No.: 10/613,428) Art Unit: 2818
Filed: July 3, 2003)
For: A Biasing Technique for a High Density SRAM	<u></u>
Commissioner for Patents P.O. Box 1450 Alexandria, Virginia 22313-1450	
<u>INFORMATION DISC</u> Sir:	CLOSURE STATEMENT
Enclosed is a copy of Information D	Disclosure Citation Form PTO-1449 or
PTO/SB/08 together with copies of the doc	uments cited on that form. It is respectfully
requested that the cited documents be consi	dered and that the enclosed copy of
Information Disclosure Citation Form PTO	-1449 or PTO/SB/08 be initialed by the
Examiner to indicate such consideration an	d a copy thereof returned to applicant(s).
I hereby certify that this correspondence is being deposite sufficient postage in an envelope addressed to the Commit 22313-1450 on October 17, 2003	ed with the United States Postal Service as first class mail with issioner for Patents, P.O. Box 1450, Alexandria, Virginia
(Date	e of Deposit)
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Pursuant to 37 C.F.R. § 1.97, this Information Disclosure Statement is being submitted under one of the following (as indicated by an "X" to the left of the appropriate paragraph):

<u>X</u>	37 C.F.R. §1.97(b).
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	A statement pursuant to 37 C.F.R. §1.97(e) or
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- (1) A statement pursuant to 37 C.F.R. §1.97(e); and
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If there are any additional charges, please charge Deposit Account No. 02-2666.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Dated: October 17, 2003

Mark L. Watson Reg. No. 46,322

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Substitute for Form 1449/PTO					Complete if Known			
OVE ONFORMATION DISCLOSURE OCT 2 0 2003 STATEMENT BY APPLICANT					Application Number	10/613,428		
					Filing Date	07/03/2003		
					First Named Inventor:	Zhang et al.		
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7>	.0				Examiner Name			
Sheepen	1		of	2	Attorney Docket Number	42P13001C		
-	<u> </u>		U.S. PAT	ENT DOCUMENTS				
Examiner Cite Initials*	Cite No.	Document Number		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant		
		Numb	er-Kind Code ² (If known)			Passages or Relevant Figures Appear		
		US-	6,301,146	10/09/2001	Ang et al.			
		US-	6,163,476	12/19/2000	Marr et al.			
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NIFORMATION DISCLOSURE			Application Number 10/613,428							
			Filing Date		07/03/2003					
STATEMENT BY APPLICANT (use as many sheets as necessary)		First Named Inventor:		Zhang et al.						
(use as many sheets as necessary)			Art Unit	2818						
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Sheet	2		of	2		Attorney Docket Number		42P13001C		
				NON PATE	NT LIT	ERATURE DOCUMENTS	3			
Examiner Initials*	Cite No ¹								T ²	
		Noda K et al., "1.9-MUM 2 Loadless CMOS Four-Transistor SRAM Cell in a 0.18-MUM Logic Technology," International Electron Devices Meeting, 1998. IEDM Technical Digest, pp 643-646.								
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